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Design_implementation, and Evaluation of...-Ishizaki...(1999) (Correct) (2 citations) Kawahito, Toshiaki Yasue, Mikio Takeuchi, Takeshi Ogasawara, Toshio Suganuma, Tamiya Onodera, Hideaki www.cse.ucsd.edu/classes/sco0/cse231/poeniit.tof

Optimizing Precision Overhead for x86 Processors - Ogasawara, Komatsu, Nakatani (Correct)
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